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016295.1509
(DC-05592)

PATENT APPLICATION
10/753,615

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CLAIM AMENDMENTS

IN THE CLAIMS

This listing of the claims will replace all prior versions, and listing, of claims in the application or previous response to office action:

1-15. (Previously cancelled)

16. (Currently amended) An information handling system comprising:

a processor;

a memory communicatively coupled to the processor; and

a circuit board communicatively coupled to the processor, the circuit board comprising:

a voltage plane forming a first layer of the circuit board, the voltage plane operable to provide an electrical current;

a ground plane forming a second layer of the circuit board, the ground plane operable to provide a ground for the electrical current;

~~the an electrical trace, electrically distinct from the voltage plane and the ground plane, routed over a portion of the circuit board, the electrical trace~~ including

a first path and a second path such that the first path references the ground plane and the second path references the voltage plane whereby the first path is substantially similar to the second path; and

the first path electrically coupled to the second path at proximal to each of the ends of the paths.

17. (Original) The information handling system of Claim 16, wherein the first path is located at a distance from the ground plane that is substantially equal to the distance the second path is located from the voltage plane.

18. (Original) The information handling system of Claim 16, wherein the ground plane and the voltage plane are symmetrically oriented about the circuit board.

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19. (Original) The information handling system of Claim 18, wherein the first path and the second path are symmetrically oriented about the circuit board.

20. (Original) The information handling system of Claim 19, wherein the first path and the ground plane are a mirror image of the second path and the voltage plane.

21. (New) An information handling system comprising:

an integrated circuit; and

a circuit board communicatively coupled to the integrated circuit, the circuit board comprising:

an electrically conductive voltage plane forming a first layer of the circuit board;

an electrically conductive ground plane forming a second layer of the circuit board;

and

an electrically conductive trace not in electrical contact with the voltage plane or the ground plane, comprising:

a first portion lying in a first plane that references the ground plane;

a second portion lying in a second plane that references the voltage plane;

a first electrically conductive via contacting a first point of the first portion

and contacting the a first point of the second portion; and

a second electrically conductive via contacting a second point of the first portion and contacting a second point of the second portion.

22. (New) The information handling system of claim 21, wherein first and second portions mirror each other in their respective planes.

23. (New) The information handling system of Claim 21, wherein a displacement between the first plane and the ground plane is substantially equal to a displacement between the second plane and the voltage plane.

24. (New) The information handling system of Claim 21, wherein the ground plane and the voltage plane are equidistant from a center plane of the circuit board.

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25. (New) The information handling system of Claim 21, wherein the first plane and a plane the second plane are equidistant from a center plane of the circuit board.

26. (New) An information handling system, comprising:
an integrated circuit; and

a circuit board coupled to the integrated circuit, the circuit board comprising:

an electrically conductive voltage plane operable to receive a supply voltage;

an electrically conductive ground plane operable to receive a ground voltage; and

an electrically conductive trace operable to receive a data signal, comprising:

a first portion lying in a first plane that references the ground plane;

a second portion lying in a second plane that references the voltage plane;

a first electrically conductive via contacting a first point of the first portion

and contacting the a first point of the second portion; and

a second electrically conductive via contacting a second point of the first

portion and contacting a second point of the second portion.

27. (New) The information handling system of Claim 26, wherein first and second portions mirror each other in their respective planes.

28. (New) The information handling system of Claim 26, wherein a displacement between the first plane and the ground plane is substantially equal to a displacement between the second plane and the voltage plane.

29. (New) The information handling system of Claim 26, wherein the ground plane and the voltage plane are equidistant from a center plane of the circuit board.

30. (New) The information handling system of Claim 26, wherein the first plane and the second plane are equidistant from a center plane of the circuit board.

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31. (New) The information handling system of Claim 26, wherein the supply voltage is a DC voltage and the data signal is a time varying signal.

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